

Notice of References Cited	Application/Control No. 10/608,028	Applicant(s)/Patent Under Reexamination IZUMITANI, JUNKO	
	Examiner Eric B. Chen	Art Unit 1765	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-6,340,435	01-2002	Bjorkman et al.	216/72
	B	US-5,478,436	12-1995	Winebarger et al.	438/693
	C	US-6,387,821	05-2002	Aoki, Hidemitsu	438/745
	D	US-6,479,443	11-2002	Zhang et al.	510/175
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 1986, Lattice Press, Vol. 1, pp. 191, 534.
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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